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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10715440
	Filing Date	2003-11-19
	First Named Inventor	Monier Maher
	Art Unit	2123
	Examiner Name	Kidest Bahta
	Attorney Docket Number	NVDA/AG-08-0114-US

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4887235		1989-12-12	HOLLOWAY, et al.	
	2	4933846		1990-06-12	HUMPHREY, et al.	
	3	5404522		1995-04-04	CARMON, et al.	
	4	5692211		1997-11-25	GULICK, et al.	
	5	5732224		1998-03-24	GULICK, et al.	
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	7	5870627		1999-02-09	O'TOOLE, et al.	
	8	5898892		1999-04-27	GULICK, et al.	

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9	5063498		1991-11-05	HIRAHARA, et al.	
10	6366998		2002-04-02	MOHAMED, Moataz A.	
11	6754732		2004-06-22	DIXON, et al.	

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U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20010016883		2001-08-23	MINO, Yoshiteru	

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FOREIGN PATENT DOCUMENTS

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	1							<input type="checkbox"/>

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NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	Office Action, Ser. No. 10/715,459, dated January 5, 2010.	<input type="checkbox"/>

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2	TEITELBAUM, K. "Crossbar Tree Networks for Embedded Signal Processing Applications", This paper appears in: Massively Parallel Processing, 1998, Proceedings. Fifth International Conference on; Publication Date: 15-17 Jun 1998; On page(s): 200-207.	<input type="checkbox"/>
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Examiner Signature	/Kidest Bahta/	Date Considered	05/24/2010
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